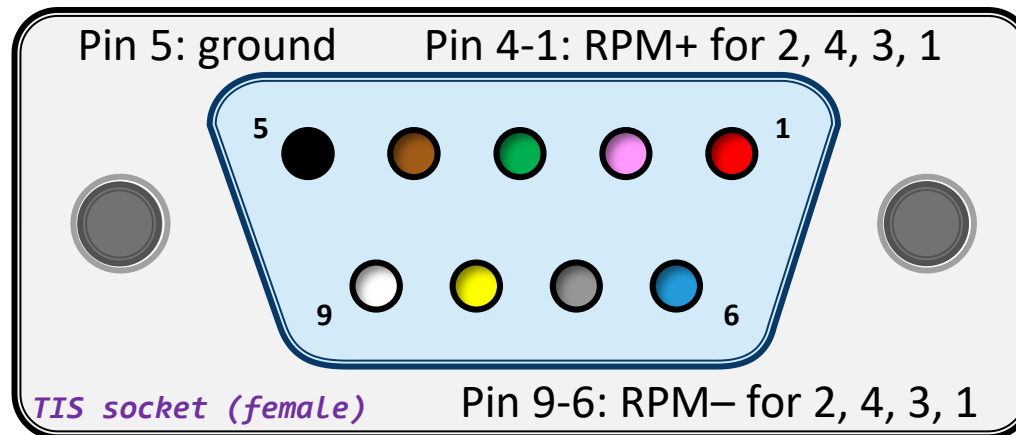
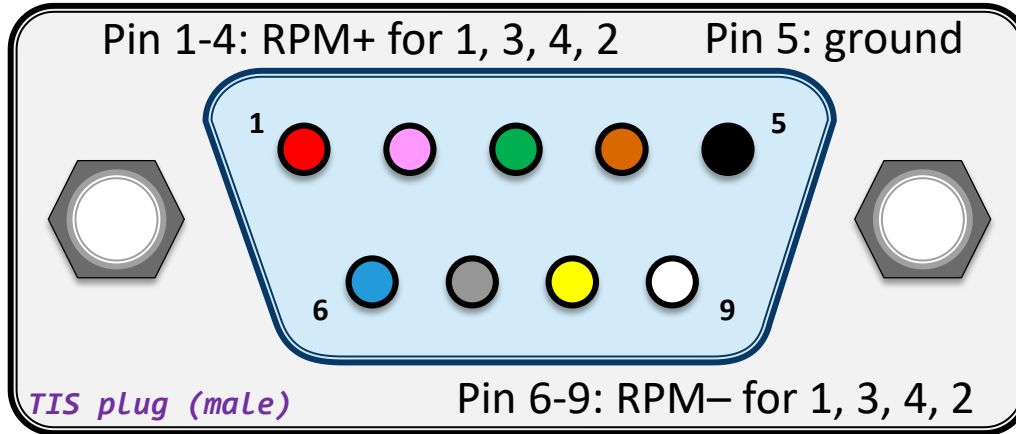


TIS connector pin layout



pin	TIS channel
1 / 6	A.0.1
2 / 7	A.0.3
3 / 8	A.0.4
4 / 9	A.0.2
5	(ground)

TIS channel	pin
A.0.1	1 / 6
A.0.2	4 / 9
A.0.3	2 / 7
A.0.4	3 / 8

*RS422 differential input signals
TTL-signal as twisted pair lines*